

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/575,381	GSCHOSSMANN ET AL.	
		Examiner	Art Unit	Page 1 of 1
		Dharti H. Patel	2836	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2004/0016461	01-2004	Qu et al.	137/554
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	J	US-			
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FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Morsch, Joachim; Circuit arrangement for protecting electronic circuits against overvoltage; pages 1-2, Fig. 1 and 2
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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